



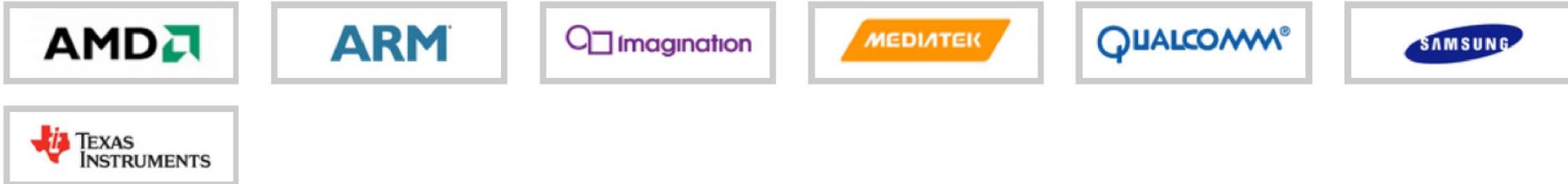
HSA 

BENJAMIN COQUELLE
MAY 2015

HAS MEMBERS



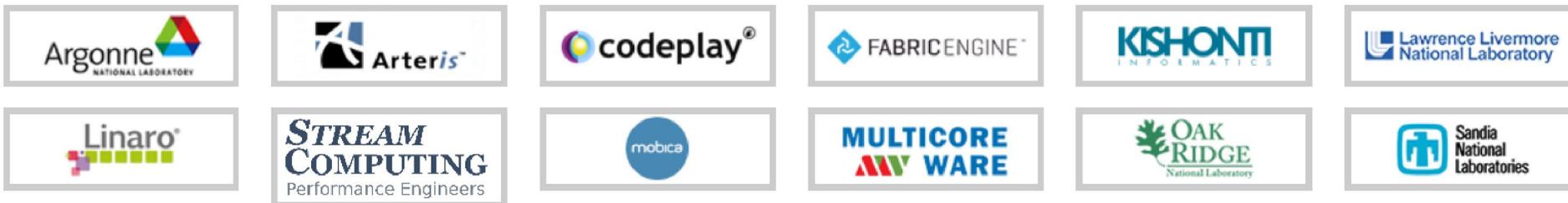
Founders



Promoters



Supporters



Contributors

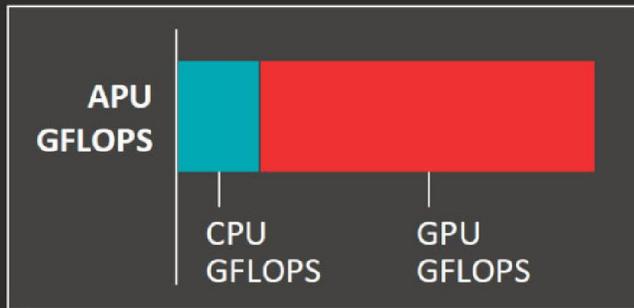


Academics



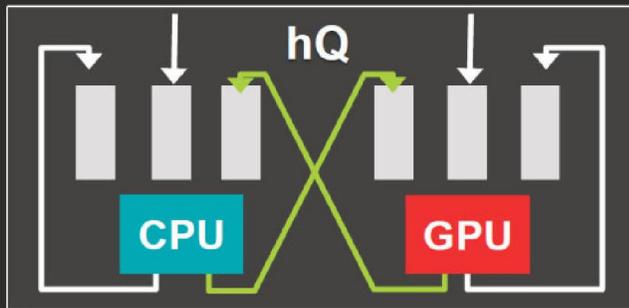
- ▲ HSA means Heterogeneous System Architecture
- ▲ Defines a set of low level software components (HSAIL, memory management...)
- ▲ Manages work queues to the HSA devices (fast enqueue and signaling)
- ▲ Gives simple access to CPU and GPU in the APU
 - Single address space for CPU and GPU
- ▲ High level languages and compiler target HSA runtime and hardware to expose all the features to all the developers
 - OpenCL 2.0, C++, Fortran, OpenMP 4,....
- ▲ <http://www.hsafoundation.com/>
- ▲ <https://github.com/hsafoundation>
 - OpenMP and C/C++ : <https://github.com/HSAFoundation/HSA-OpenMP-GCC-AMD>
 - HSA runtime : <https://github.com/HSAFoundation/HLC-HSAIL-Development-LLVM>
 - HSAIL-LLVM : <https://github.com/HSAFoundation/HSA-Runtime-Reference-Source>
 -

UNLOCKING ALL APU GFLOPS



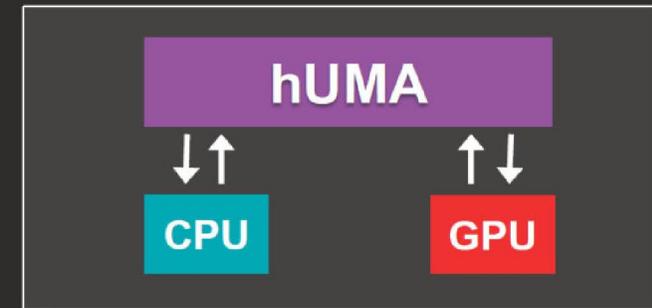
- ▲ Access to full potential of APU compute power

ALL CORES EQUAL



- ▲ Heterogeneous queuing (hQ) defines how processors interact equally
- ▲ GPU and CPU have equal flexibility to create/dispatch work

EQUAL ACCESS TO ENTIRE MEMORY



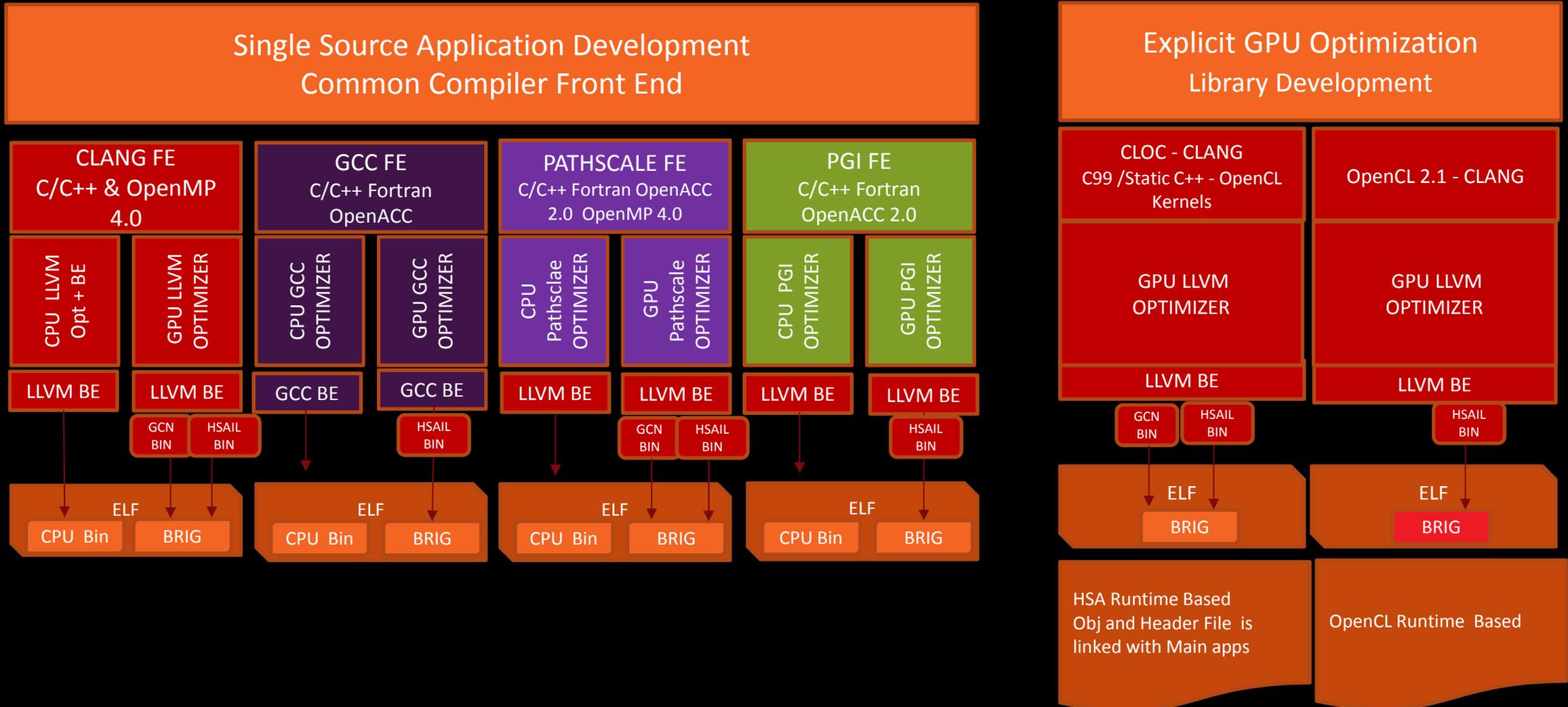
- ▲ First time ever: GPU and CPU have uniform visibility into entire memory space

UNLOCKS THE UNMATCHED COMPUTE POTENTIAL AND EFFICIENCY OF APUS

SIMPLIFIED DEVELOPMENT - GIVING YOU CONTROL WHEN YOU NEED IT



PARTNERS WILL LEVERAGE OUR CODE GENERATORS



- ▲ An Open Source implementation contributed by MulticoreWare
- ▲ Based on Clang & LLVM 3.3 and 3.5
- ▲ Lower C++ AMP codes to:
 - OpenCL SPIR
 - OpenCL C
 - HSA HSAIL / BRIG
 - x86-64 (CPU fallback)
- ▲ Compatible with all major OpenCL stacks, and HSA
- ▲ <https://bitbucket.org/multicoreware/cppamp-driver-ng/wiki/Home>

OPENCL 2.0 MEMORY MANAGEMENT

FINE GRAIN BUFFER SVM



```
//CL_MEM_SVM_FINE_GRAIN_BUFFER means host and device can
//concurrently access the buffer, thus no more data
//transfer...

float* Buffer = (float*)clSVMAlloc(ctx, CL_MEM_READ_WRITE |
                                   CL_MEM_SVM_FINE_GRAIN_BUFFER,
                                   1024 * sizeof(float), 0);

//fill the buffer from host, no data transfer
for (int i=0; i<1024; i++)
    Buffer[i] = ...;

// use your SVM buffer in you OpenCL kernel on device
directly

clSetKernelArgSVMPointer(my_kernel, 0, Buffer);

clEnqueueNDRangeKernel(queue, my_kernel,...)
```

```
//create device buffer

cl_mem DeviceBuffer = clCreateBuffer(ctx,
                                     CL_MEM_READ_WRITE, 1024*sizeof(float), NULL, &err
);

//create host buffer

float* hostBuffer = new float[1024];
for (int i=0; i<1024; i++)
    hostBuffer [i] = ...;

//data transfer happens here
clEnqueueWriteBuffer(queue, DeviceBuffer,... , hostBuffer);

//use our device buffer on device

clSetKernelArg(my_kernel,0,sizeof(cl_mem), &DeviceBuffer );

clEnqueueNDRangeKernel(queue, my_kernel,...)
```

OPENCL 2.0 MEMORY MANAGEMENT

FINE GRAIN SYSTEM



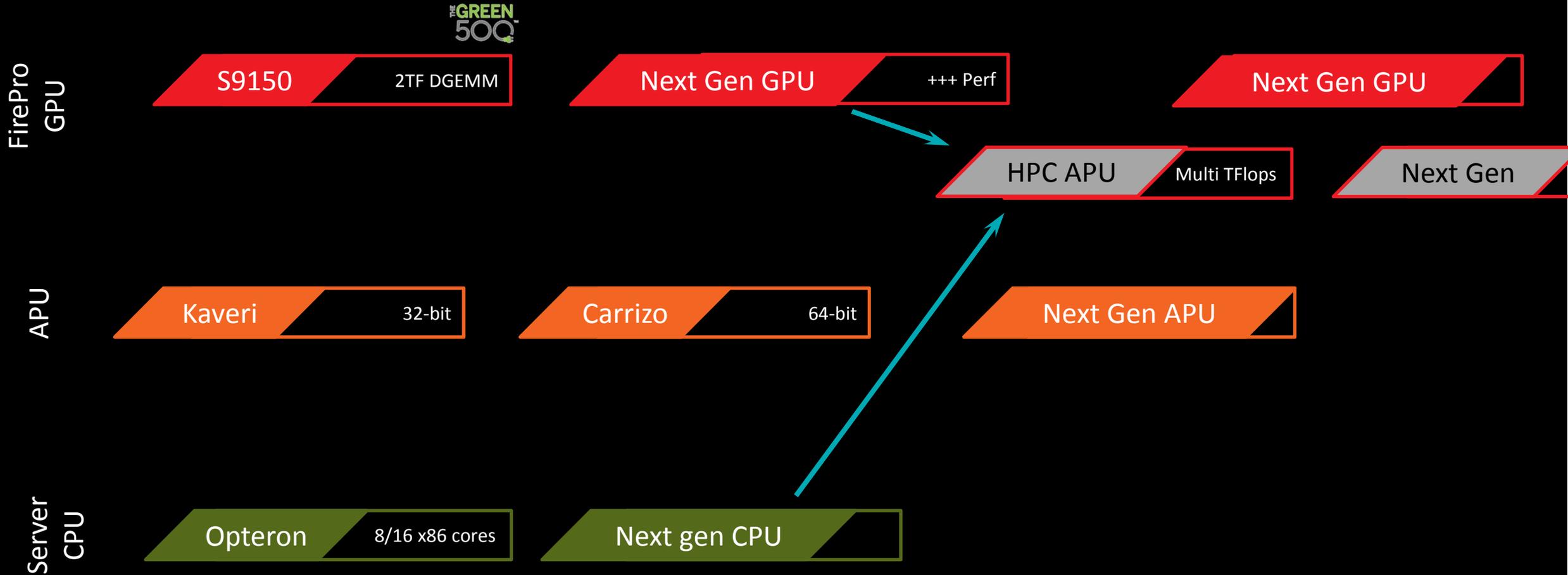
```
//no more OpenCL API needed to allocate data, simply use your favorite memory allocation function : new, malloc...
float* Buffer = (float*)malloc(1024*sizeof(float))

//fill the buffer from host, no data transfer
for (int i=0; i<1024; i++)
    Buffer[i] = ...;

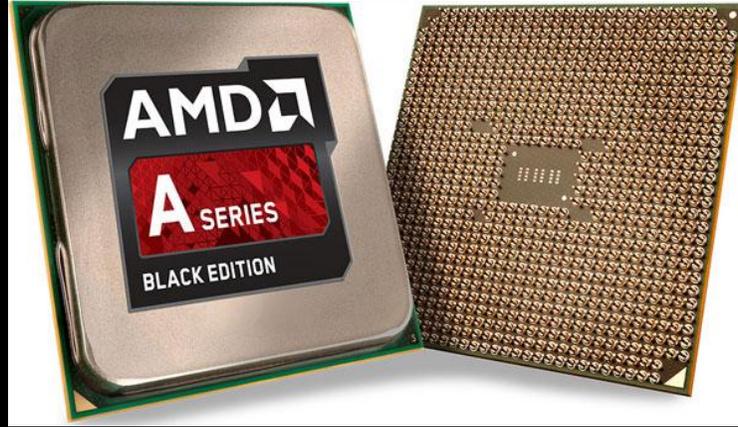
// use your SVM buffer in you OpenCL kernel on device directly
clSetKernelArgSVMPointer(my_kernel, 0, Buffer);

clEnqueueNDRangeKernel(queue, my_kernel,...)
```

AMD HPC Roadmap Trends



▲ Kaveri APU – 2014



▲ Carrizo APU – 2015

– <http://www.amd.com/en-us/press-releases/Pages/amd-discloses-architecture-2015feb23.aspx>

▲ “HPC APU” – beyond 2016

– Multi-Teraflops APU for servers and workstation

Develop your code now for tomorrow's platforms



▲ Now

Best performance with FirePro GPU

▲ Summer 2015

AMD Carrizo APU x86 64-bit laptop for code testing

THE GREEN
500™

Hawaii

44 GCN CUs

4 TF SGEMM, 2 TF DGEMM GPU

Carrizo

4 x86 cores
8 GCN CUs

x86 64-bit APU

HPC APU

Multi TFlops

Multi Tflops x86 64-bit APU

Code portability



Introducing AMD Compute Toolkit (ACT)

Performance, Power, Productivity

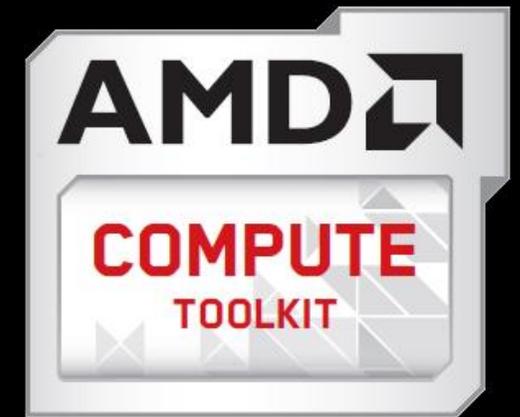
Coming
soon



- ▲ Comprehensive developer solution providing ultimate performance and power efficiency while enhancing productivity
- ▲ ACT includes
 - AMD Compute SDK supporting the latest OpenCL APIs from Khronos
 - AMD CodeXL - a tool suite with leading-edge performance and power profilers, code analyzers and debuggers
 - AMD's accelerated compute libraries
 - Documentation and training to get you up to speed fast
 - Developer support forums to get your questions answered quickly
- ▲ Find everything you need in one place with ACT

Delivering full potential from the
heterogeneous resources on your platform

<http://developer.amd.com/>



AMD CodeXL 1.7



- ▲ CodeXL helps SW developers get the best performance on AMD platforms
- ▲ Debug, Profile and Analyze applications
 - On local and remote hosts
- ▲ Power Profiling
- ▲ AMD CPUs, GPUs and APUs
- ▲ Multiple platforms and Operating Systems
 - Standalone application for Windows[®] and Linux[®]
 - Integrated into Microsoft[®] Visual Studio[®]
- ▲ Free to download and use



<http://developer.amd.com/tools-and-sdks/opencl-zone/codexl/>

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